

WHAT IS CLAIMED IS:

1. An active matrix type display device comprising:
 - a substrate;
 - a scanning line formed on the substrate;
 - a video signal line formed on the substrate;
 - a transistor connected to the scanning signal line and the video signal line;
 - a first inverter circuit connected to the transistor and formed on the substrate;
 - a second inverter circuit connected to the first inverter circuit and formed on the substrate;
 - a third inverter circuit connected to the second inverter and formed on the substrate;
 - a pixel electrode connected to the third inverter circuit; and
 - a pair of AC power supply lines formed on the substrate,wherein the first inverter circuit and the second inverter circuit are supplied with a pair of AC voltages from the AC power supply lines.
2. An active matrix type display device according to claim 1, wherein an output of the second inverter circuit is connected to an input of the first inverter circuit.
3. An active matrix type display device according to claim 1, wherein an AC voltage applied on one line of the pair of AC power supply lines is complementary to an AC voltage applied on the other line of the pair of AC power supply lines.

4. An active matrix type display device according to claim 1, further comprising a fixed voltage line formed on the substrate,

wherein the third inverter circuit is connected to the fixed voltage line and one of the pair of AC power supply lines.

5. An active matrix type display device according to claim 4, wherein the first inverter circuit, the second inverter circuit, and the third inverter circuit are connected in series.

6. An active matrix display device according to claim 1, wherein the display device is a liquid crystal display device.

7. An active matrix display device according to claim 1, wherein the display device is an electroluminescence display device.

8. An active matrix type display device comprising:

a substrate;

a scanning line formed on the substrate;

a video signal line formed on the substrate;

a transistor connected to the scanning signal line and the video signal line;

a memory circuit connected to the transistor and formed on the substrate;

a pixel electrode connected to the memory circuit; and

a pair of AC power supply lines formed on the substrate,

wherein the memory circuit comprises a first inverter circuit which is supplied with a pair of AC voltages from the AC power supply lines.

9. An active matrix type display device according to claim 8, wherein an AC voltage applied on one line of the pair of AC power supply lines is complementary to an AC voltage applied on the other line of the pair of AC power supply lines.

10. An active matrix type display device according to claim 9, wherein the memory circuit comprises a second inverter circuit which is supplied with the pair of AC voltages from the AC power supply lines.

11. An active matrix type display device according to claim 10, wherein the first inverter circuit and the second inverter circuit are connected in series.

12. An active matrix display device according to claim 8, wherein the display device is a liquid crystal display device.

13. An active matrix display device according to claim 8, wherein the display device is an electroluminescence display device.

14. An active matrix display device comprising:

a substrate;

a scanning line formed on the substrate;

a video signal line formed on the substrate;

a transistor connected to the scanning signal line and the video signal line;

a memory circuit connected to the transistor and formed on the substrate;

a pixel electrode connected to the memory circuit; and

a pair of AC power supply lines formed on the substrate,
wherein the memory circuit is supplied with a pair of AC voltages from the AC power supply lines, and
wherein the transistor, the memory circuit, and the pixel electrode are connected in series.

15. An active matrix type display device according to claim 14, wherein the transistor and the pixel electrode are not connected directly.

16. An active matrix type display device according to claim 14, wherein an AC voltage applied on one line of the pair of AC power supply lines is complementary to an AC voltage applied on the other line of the pair of AC power supply lines.

17. An active matrix type display device according to claim 16, wherein the memory circuit comprises a first inverter circuit and a second inverter circuit, both of which are supplied with the pair of AC voltages from the AC power supply lines.

18. An active matrix type display device according to claim 17, wherein the first inverter circuit and the second inverter circuit are connected in series.

19. An active matrix display device according to claim 14, wherein the display device is a liquid crystal display device.

20. An active matrix display device according to claim 14, wherein the display device is an electroluminescence display device.